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I/O pad output impedance in transmission mode Z tr

Matching conditions:

Z TERM=Z line

R

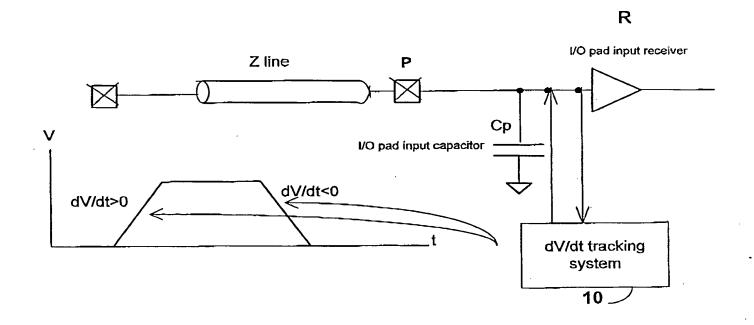
I/O pad input receiver

I/O pad input receiver

Terminated resistor

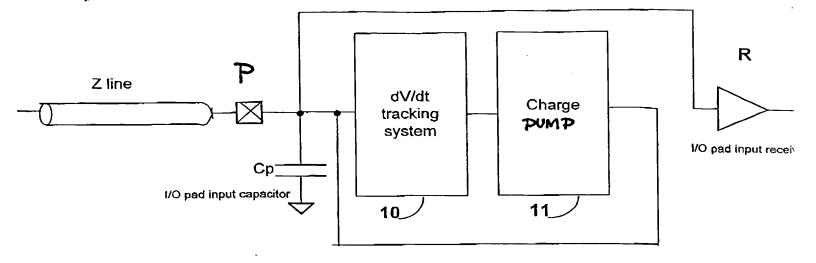
Z TERM

Cp

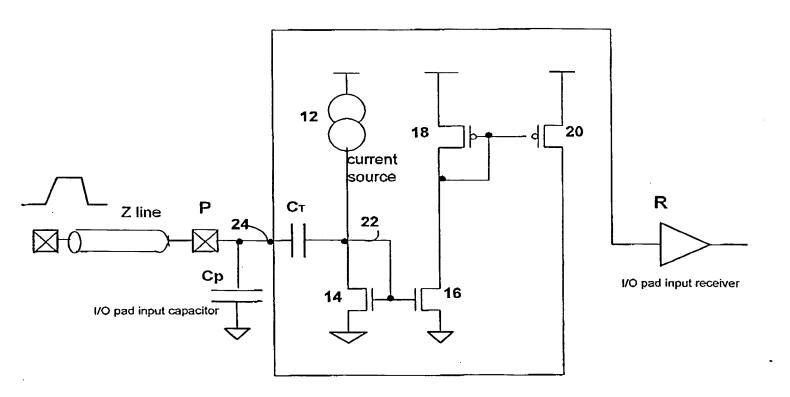


Flg2A. dV/dt tracking system

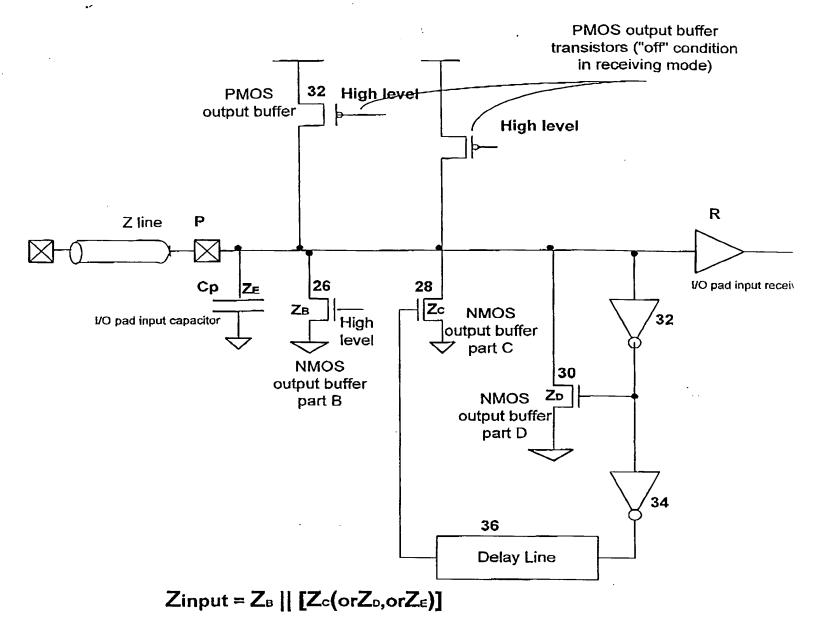
Flg1B. Parallel termination



Flg. 2 dV/dt tracking system with charge pump

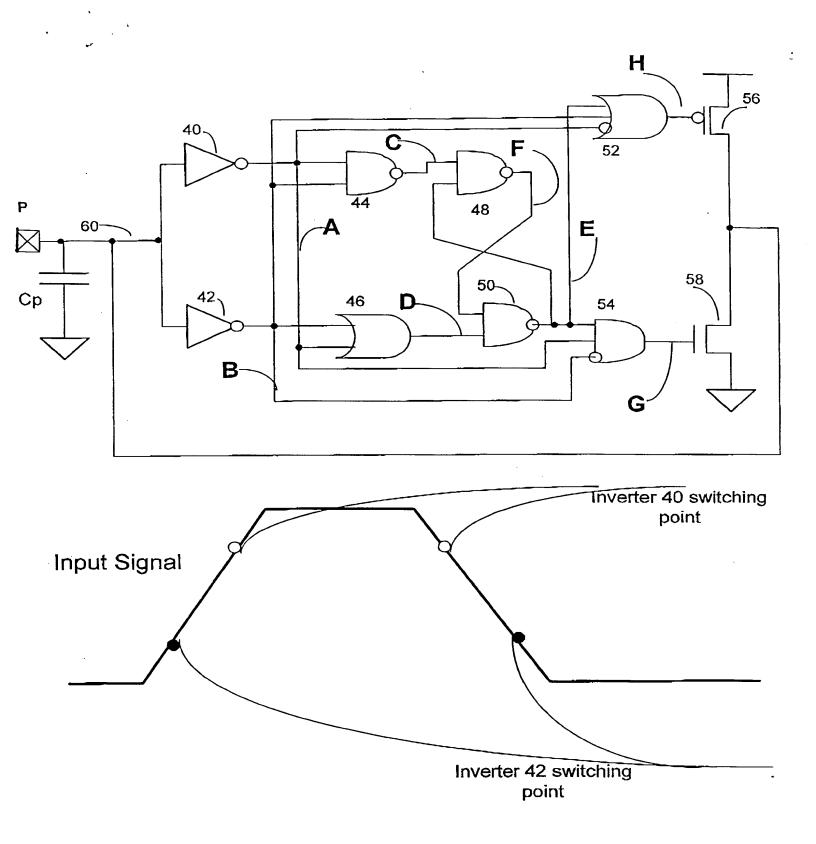


Flg. 2C. dV/dt tracking system with charge pump (more detailed)



if $|Z_C| = |Z_D| = |Z_E|$, then an input signal can not change Zinput

Flg. 2D NMOS output buffer parallel termination



Flg. 3. An input parasitic capacitor charge/discharge circuit

